

Andre Meyer

Electronic Engineer for Embedded Devices, Sensors and Imaging Systems

www.andmeyer.ch

Professional Interests

Electronics and embedded software for industrial and medical applications.

Electronics, Embedded Real-Time Systems, Imaging Systems, Sensors, Analog and Digital Circuits, Computer Architectures, Integrated Circuits (FPGA/ASIC), Communication Systems.

Education

M.S., ETH Zurich, Electrical Engineering and Information Technology, 2007.

Intensive General English Course, Exeter GB, 2002.

Experience

Electronic Engineer and Co-Founder, Werktag GmbH, Switzerland, since 2017.

Renovation of an old farmhouse (private project), 2016 - 2017.

Electronic Engineer for Medical Imaging, Varian Medical Systems, Switzerland, 2011 - 2015.

Board Member, Outdoorland AG, Switzerland, 2002 - 2012.

HW Design Engineer for Image Acquisition Devices, Leutron Vision AG, Switzerland, 2007 - 2010.

Embedded System Engineer, Leutron Vision AG, Switzerland, 2006.

Internship Embedded Software, MVTec Software GmbH, Germany, 2005.

Tutorial Assistant, ETH Zurich, Switzerland, 2005 - 2007.

Volunteer Experience

Board member in charge of skitouring, SAC-Hoernli (member of the Swiss Alpine Club), since 2018.

Trumpet player and board member, Bigband Uster, since 2015.

Coach and board member in charge of ski racing department, Skiclub RGZO, 2008 - 2012.

Publications

Practical example for the book "Digital Integrated Circuit Design" by Hubert Kaeslin, 2008.

Digital Speaker Processor, Master Thesis, ETH Zurich, 2007.

Demosaicing Optimizations for the PicSight Camera, Student Thesis, ETH Zurich, 2006.

Channel Estimation for MIMO-OFDM, Student Thesis, ETH Zurich, 2006.

Patents

High DQE Imaging Device (co-inventor), Varian Medical Systems Inc., 2017.

Projects

Adaino - Analog Data Acquisition Library for Arduino, since 2018.

Manufacturing Test Systems for X-Ray Detectors, 2011 - 2015.

Simulation of Radiation Physics for X-Ray Detectors, 2011 - 2015.

Test & Characterization of Mixed Signal ASIC for X-Ray Detector Readout, 2012 - 2014.

Mixed Signal PCB Design for Readout Module of X-Ray Sensors, 2013 - 2014.

Scintillator Enhancements for MV Imaging, 2013 - 2015.

Image Quality Characterizations for X-Ray Detectors, 2011 - 2015.

EMI Shielding of X-Ray Detector, 2011 - 2012.

Manufacturing Test Systems for Machine Vision Electronics incl. JTAG Tests, 2008 - 2010.

Embedded Software for Smart Cameras, 2008 - 2010.

Universal Image Sensor Interface, 2008 - 2010.

FPGA Designs for Industrial Cameras with DDR and PCIe Interfaces, 2007 - 2010.

Project Organization, Library and Build Management for modular FPGA Designs, 2007 - 2010.

SoC and Co-Processor Design for Digital Speaker Processor, ETH Zurich, 2007.

Demosaicing Optimizations using SIMD Instructions, ETH Zurich, 2006.

ASIC Design for Channel Estimation of MIMO-OFDM Wireless Systems, ETH Zurich, 2006.

Software Interface for FireWire Cameras, 2005.